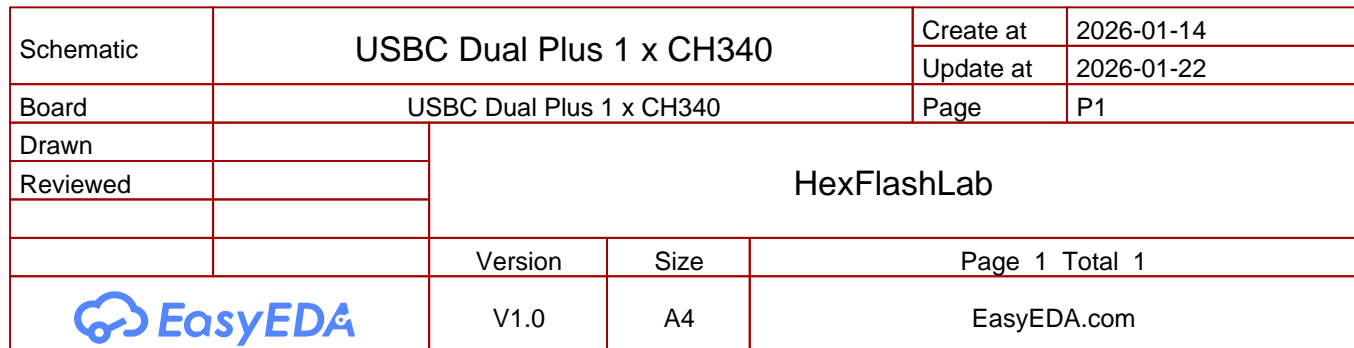
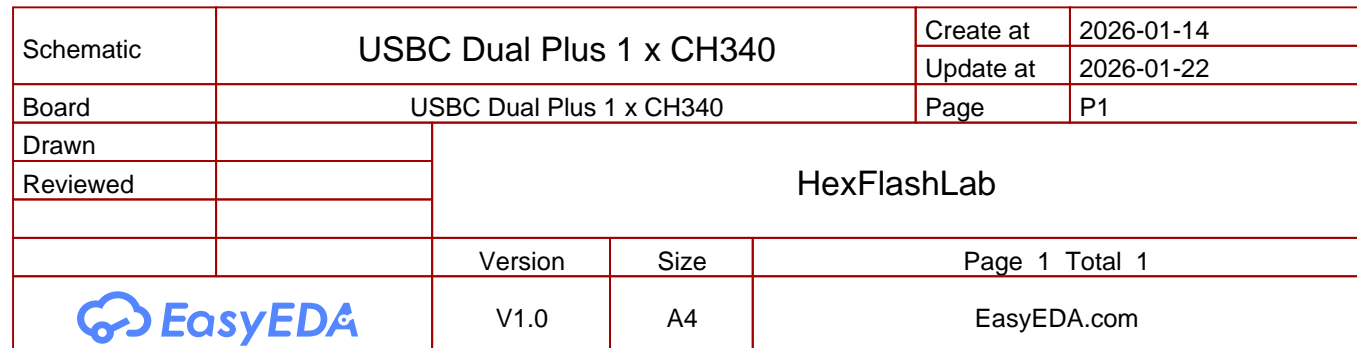
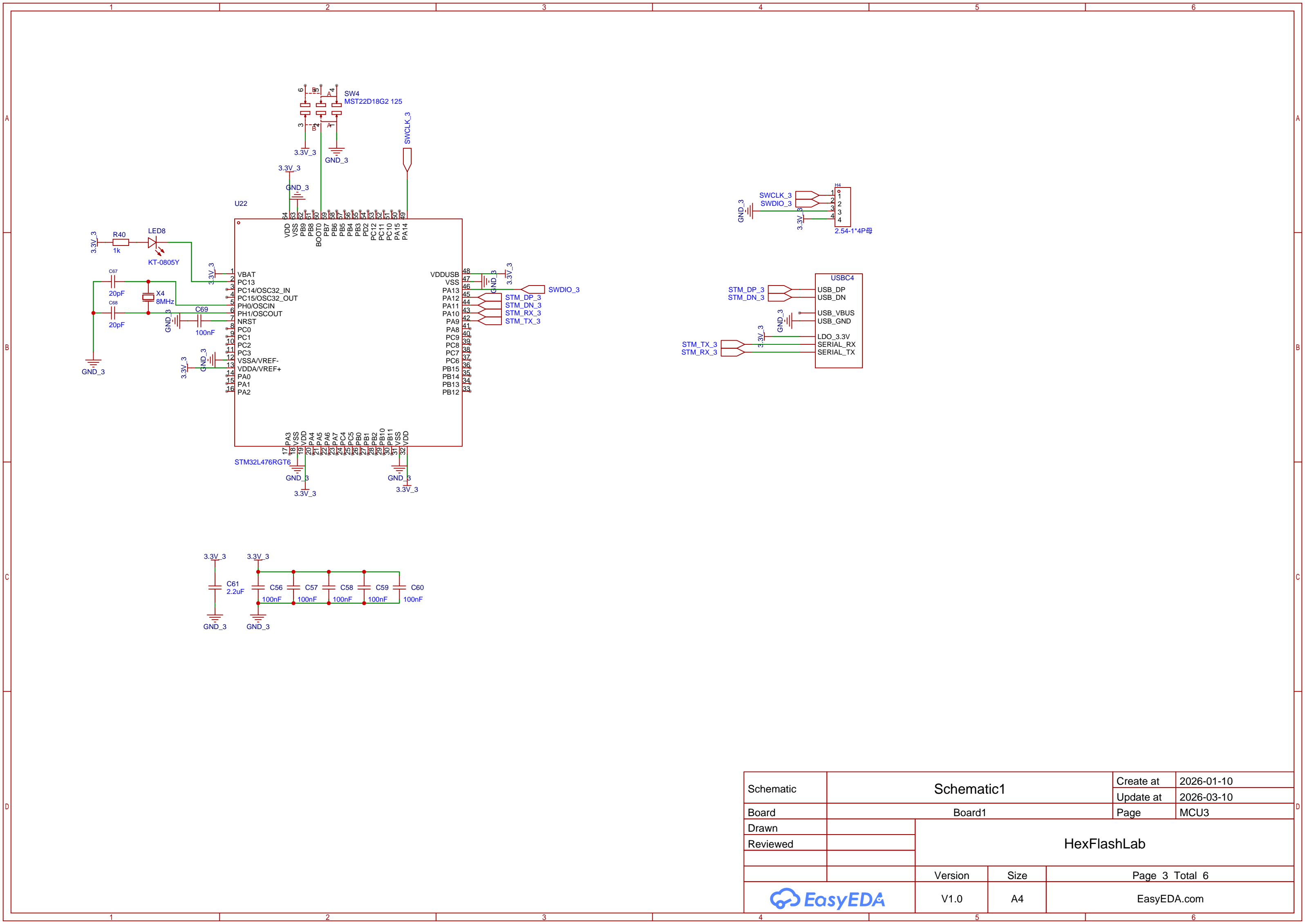


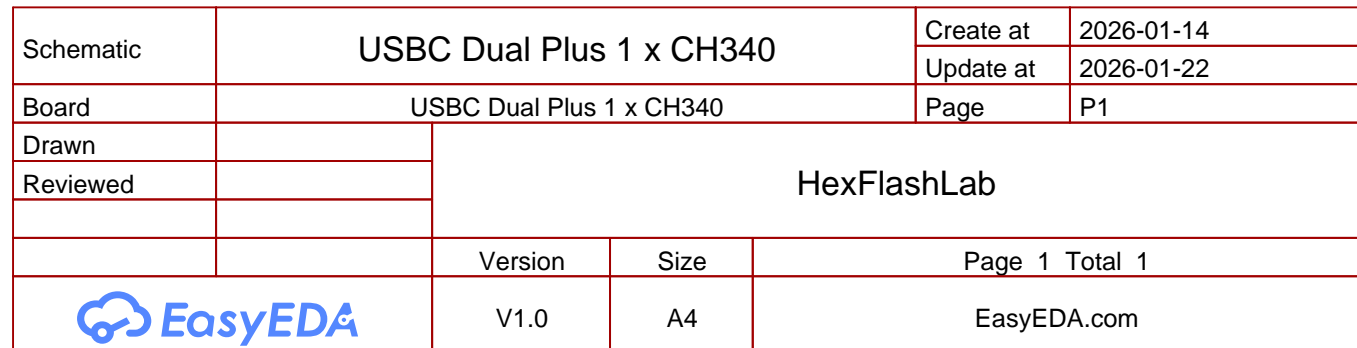
Schematic	Schematic1			Create at	2025-12-24
				Update at	2026-03-10
Board	Board1			Page	MCU1
Drawn		HexFlashLab			
Reviewed					
		Version	Size	Page 1 Total 6	
EasyEDA		V1.0	A4	EasyEDA.com	

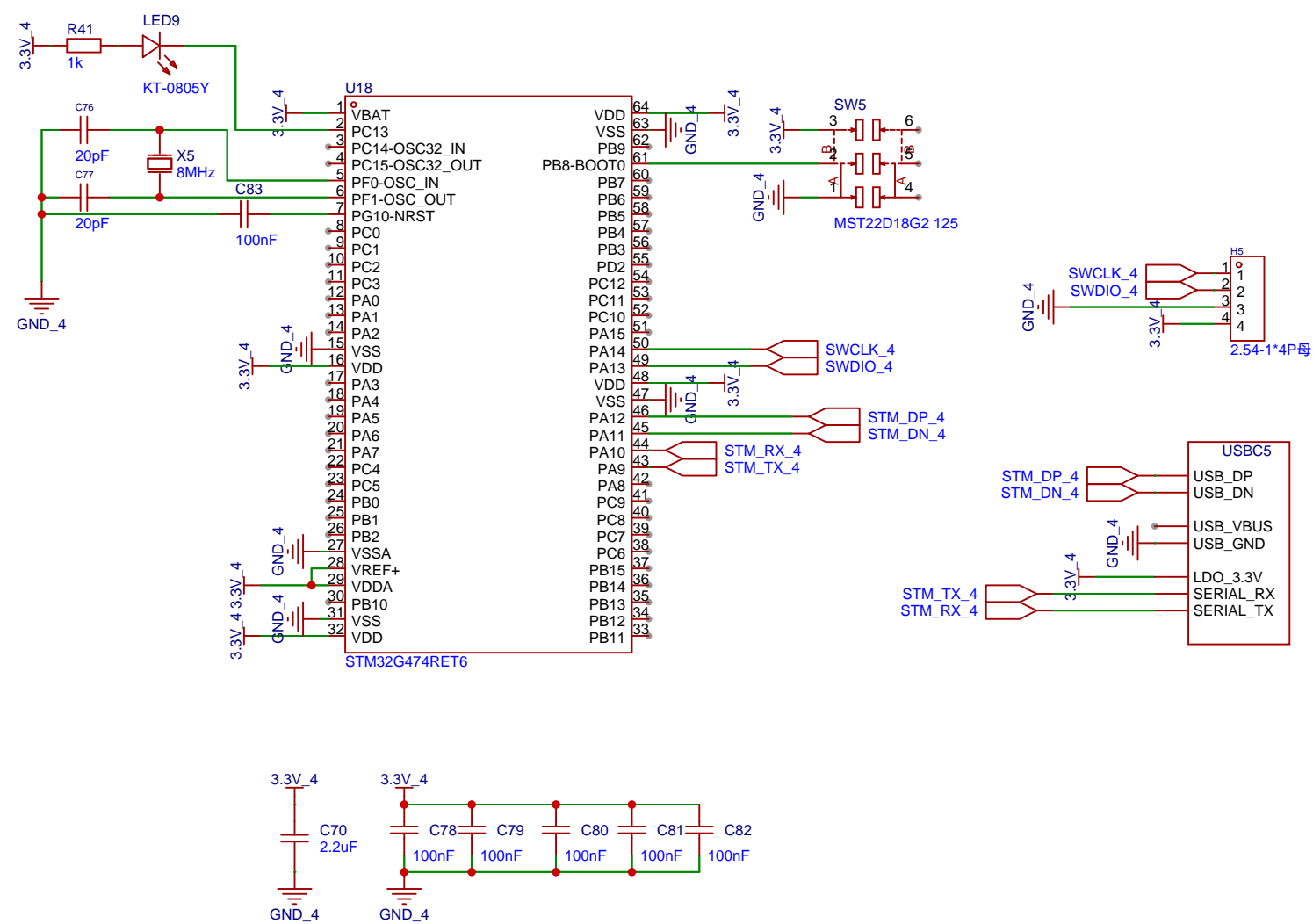





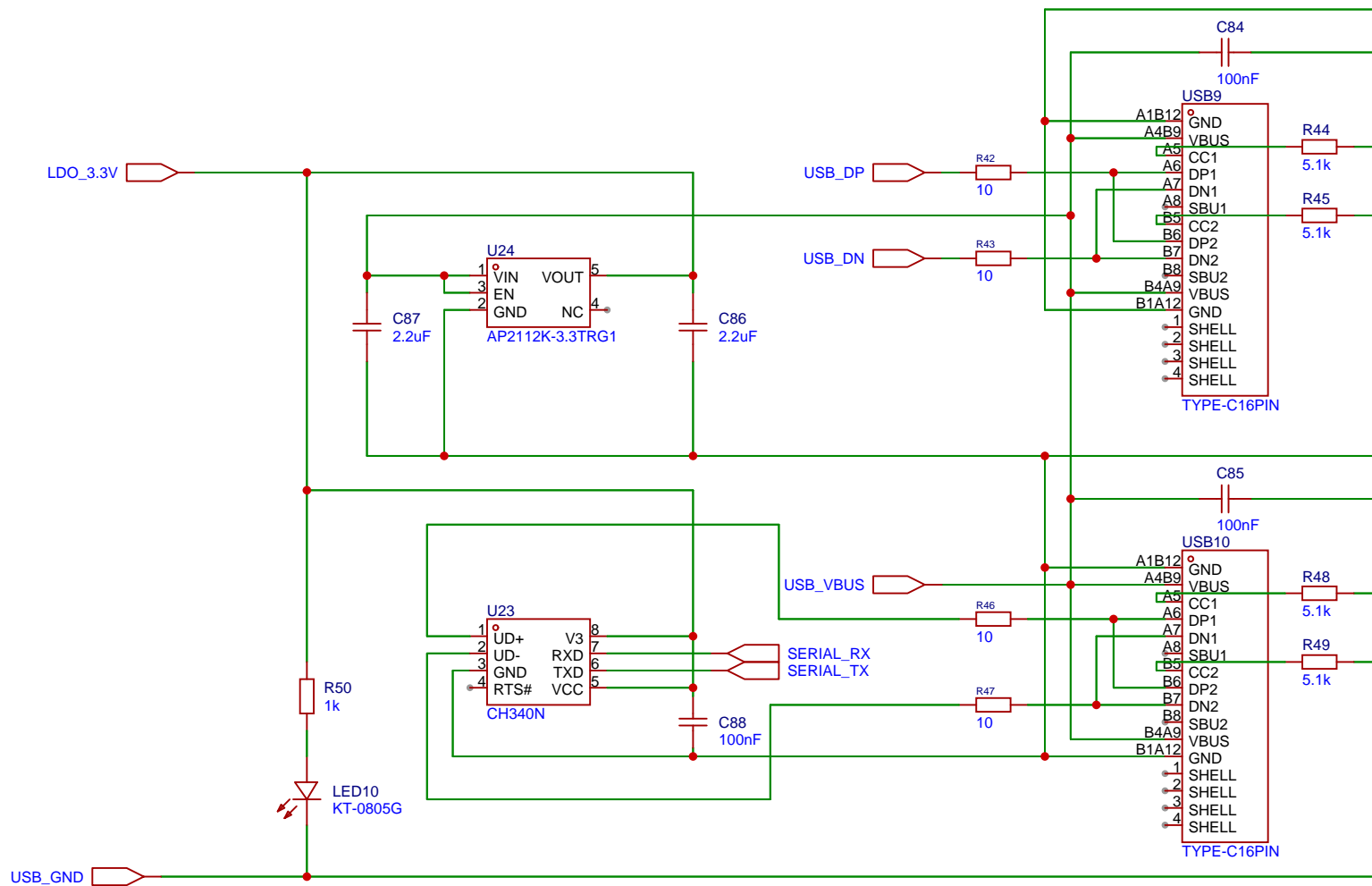



Schematic	Schematic1			Create at	2026-01-10
				Update at	2026-03-10
Board	Board1			Page	MCU3
Drawn	HexFlashLab				
Reviewed					
		Version	Size	Page 3 Total 6	
EasyEDA		V1.0	A4	EasyEDA.com	

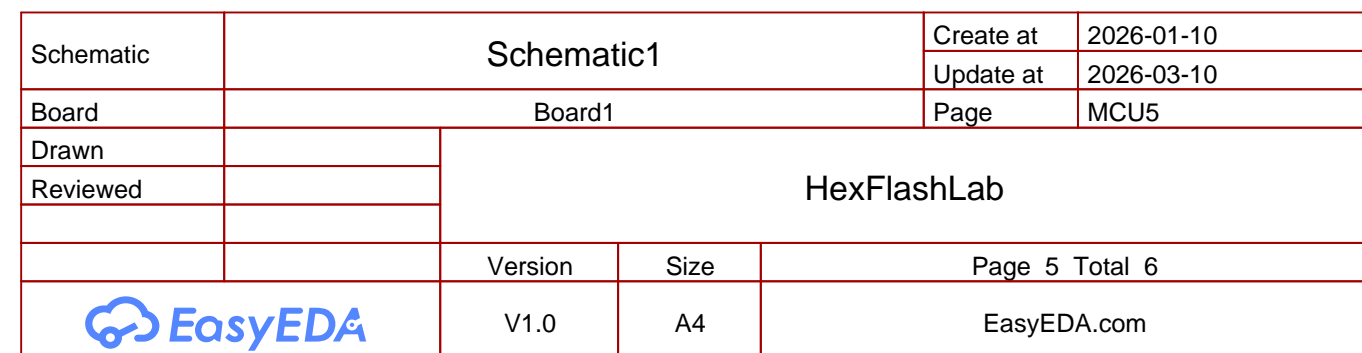


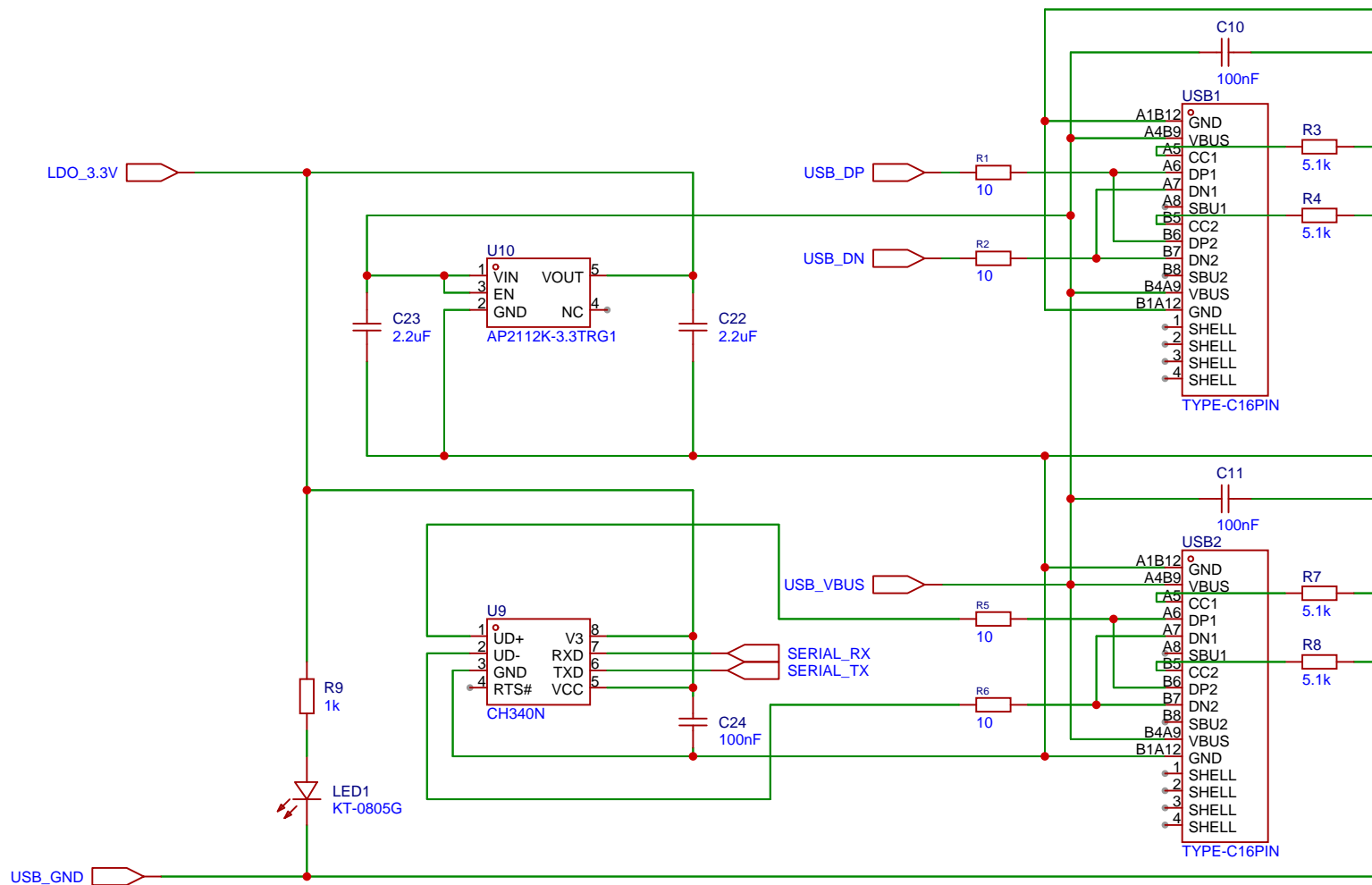



Schematic	Schematic1			Create at	2026-01-10
				Update at	2026-03-10
Board	Board1			Page	MCU4
Drawn		HexFlashLab			
Reviewed					
		Version	Size	Page 4 Total 6	
		V1.0	A4	EasyEDA.com	

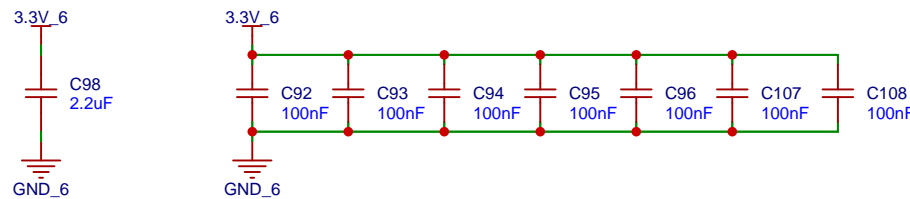
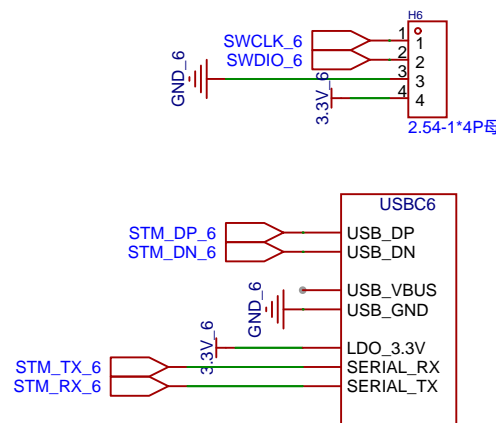
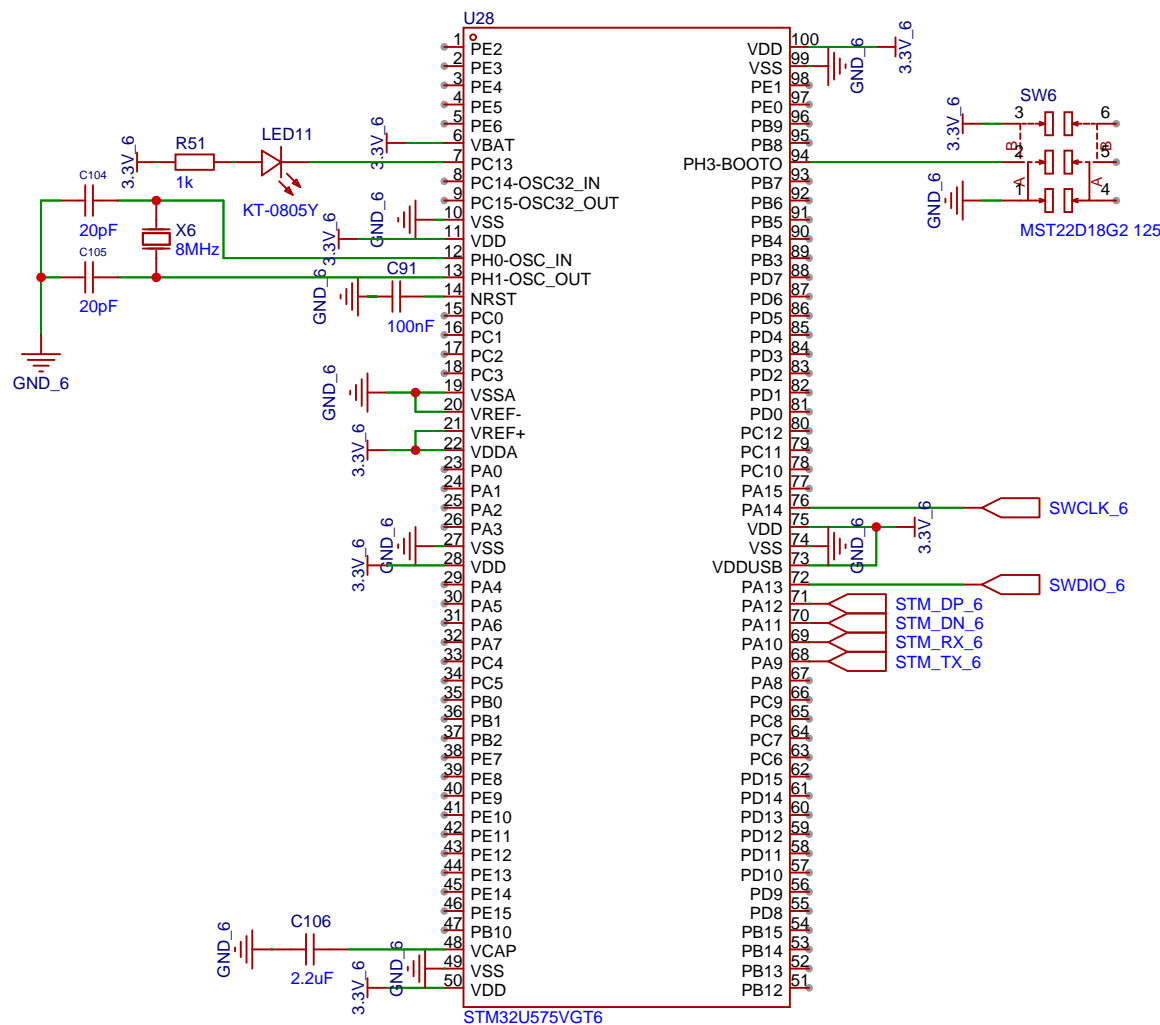


Schematic	USBC Dual Plus 1 x CH340			Create at	2026-01-14
Board	USBC Dual Plus 1 x CH340			Update at	2026-01-22
Drawn		HexFlashLab			
Reviewed					
		Version	Size	Page 1 Total 1	
		V1.0	A4	EasyEDA.com	

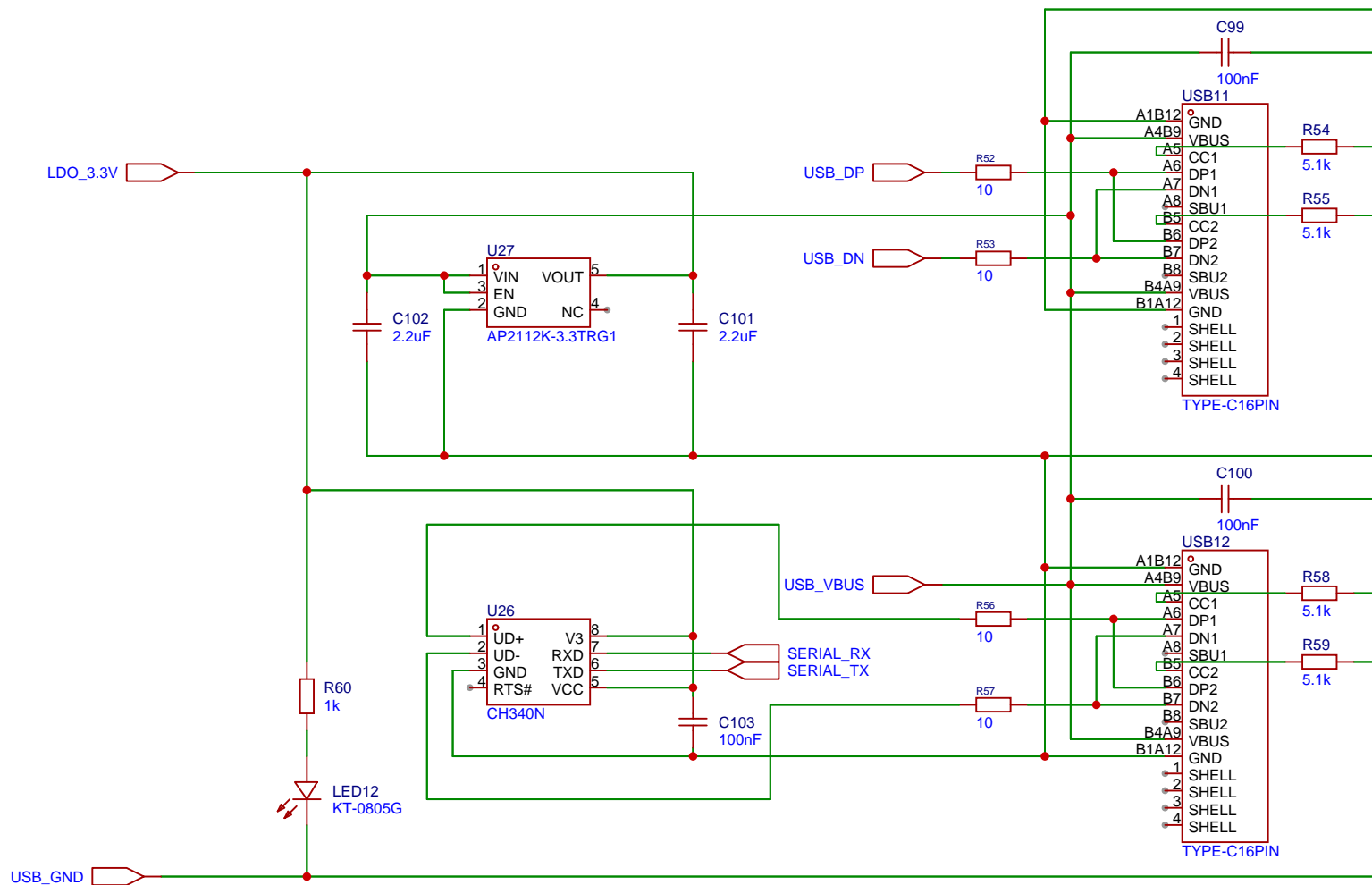





Schematic	USBC Dual Plus 1 x CH340			Create at	2026-01-14
				Update at	2026-01-22
Board	USBC Dual Plus 1 x CH340			Page	P1
Drawn		HexFlashLab			
Reviewed					
		Version	Size	Page 1 Total 1	
		V1.0	A4	EasyEDA.com	



Schematic	Schematic1	Create at	2026-01-10	
		Update at	2026-03-10	
Board	Board1		Page	MCU6
Drawn	HexFlashLab			
Reviewed				
		Version	Size	Page 6 Total 6
EasyEDA		V1.0	A4	EasyEDA.com



Schematic	USBC Dual Plus 1 x CH340			Create at	2026-01-14
Board	USBC Dual Plus 1 x CH340			Update at	2026-01-22
Drawn		HexFlashLab			
Reviewed					
		Version	Size	Page 1 Total 1	
		V1.0	A4	EasyEDA.com	